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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10033123	FILING DATE 10/22/2001	CLASS 712	SUBCLASS	GAU 2183	EXAMINER HAI NGUYEN
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**APPLICANTS: Fibranz Heiko; Plaettner Eckehard; ✓

**CONTINUING DATA VERIFIED:

None HLN

** FOREIGN APPLICATIONS VERIFIED:

GERMANY 100 52 210.6 10/20/2000 HLN

PG-PUB DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	P2000,0257
Verified and Acknowledged Examiners's initials HLN	
TITLE : Integrated circuit having a synchronous and an asynchronous circuit and method for operating such an integrated circuit	

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims 7	Print Claim for O.G. 1
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg. 2	Figs. Drwg. 2
		Print Fig. 1	
Primary Examiner		Application Examiner	
PREPARED FOR ISSUE			
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